



DE LA RECHERCHE À L'INDUSTRIE

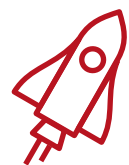
# Technology infrastructures as key enabler for a successful EU Chip's Act impact

23 june 2022

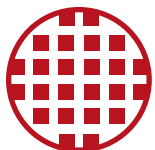
Dr Jean-Philippe Bourgoin



Created in **1967** in Grenoble, France  
**2,000+** people in 2021



**3,000+** patents in portfolio  
72 startups created over 20 years  
4000 new jobs



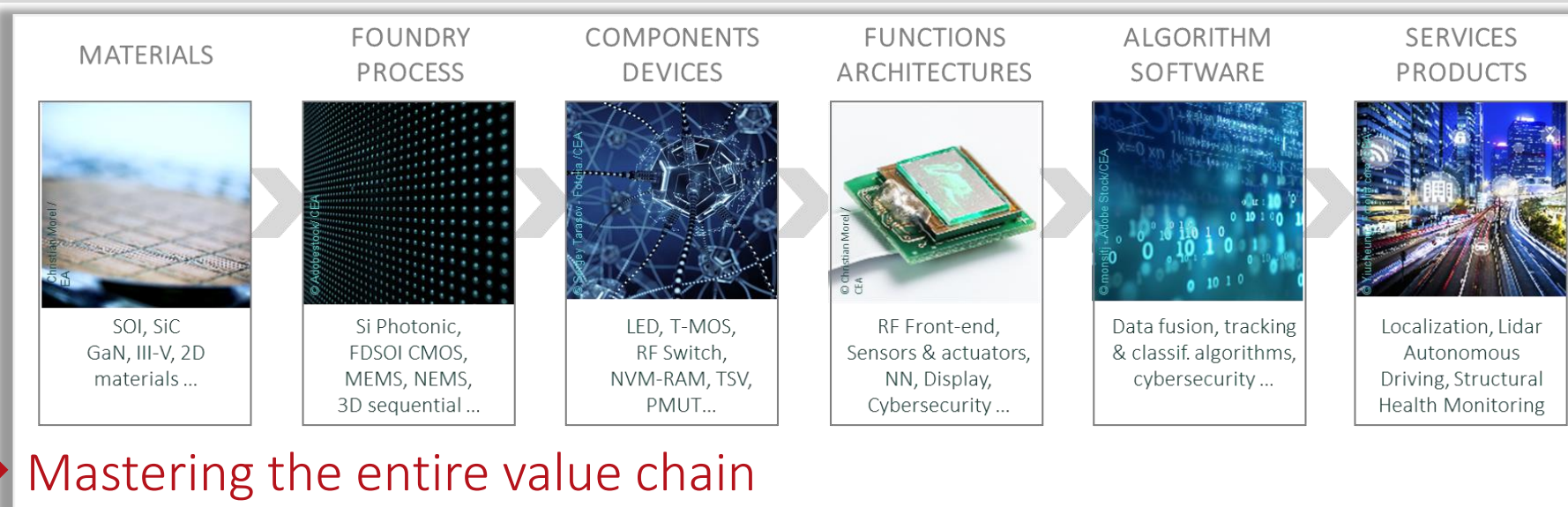
500 state-of-the-art tools  
11,000 m<sup>2</sup> of cleanroom space

**330M€** of budget (85% from contracts)

World class  
technology infrastructures  
@Grenoble



World class  
technology infrastructures  
@Grenoble



e.g. Regional :42k People in Digital sector  
µelectronics; imaging; displays



e.g. supporting FDSOI development



e.g. contributing to the EU ecosystems



→ Nurturing complete ecosystems



# 3 complementary Pilots lines to build the ambition of Europe in the next decade

## Example of the 10 nm FDSOI and beyond CEA Pilot line

RTO alliance for technology and innovation leadership

**Pilot Line for advanced nodes**  
finFET, GAA and beyond:  
10nm → sub-2nm for logic and memory incl. leading edge equipment & 3D-SOC (chiplet) demonstration

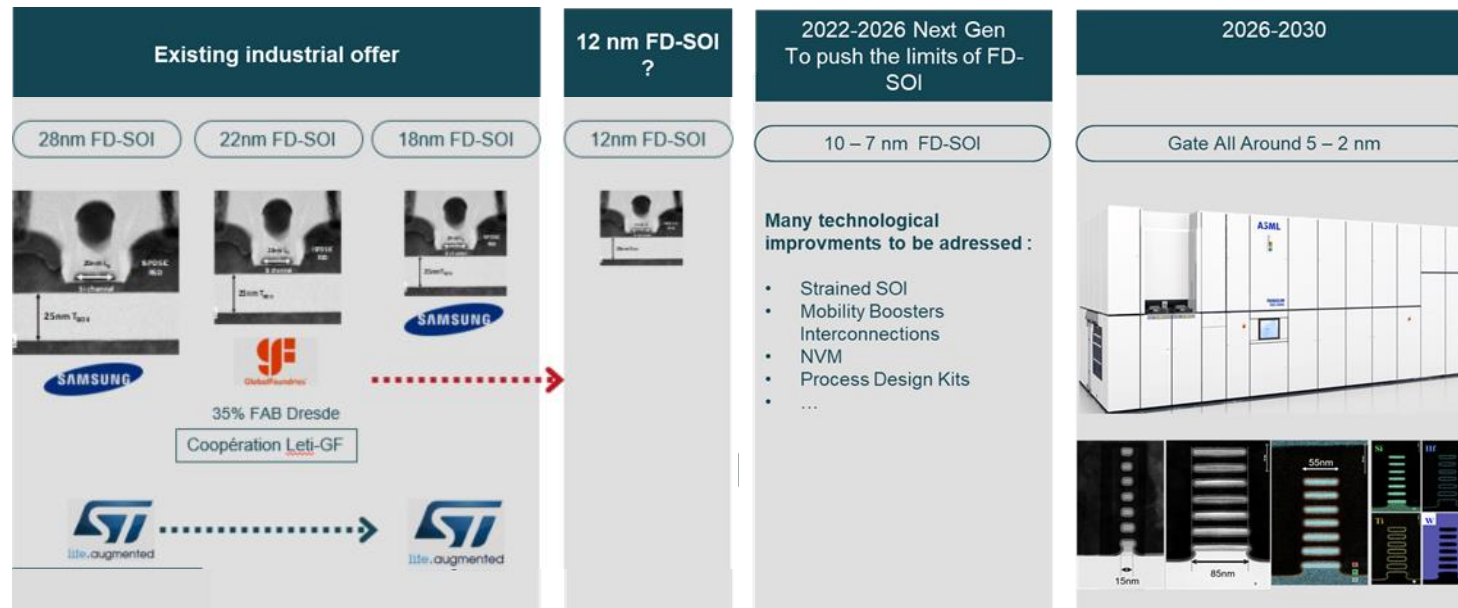
EUROPEAN NEW SYSTEM CONCEPT DEMONSTRATION

**Pilot Line for Design & System integration**  
Design & design technology for adv. CMOS, Advanced simulation capabilities on devices, circuits, & systems, chiplets ...  
System integration technologies for SoC integration

imec

leti  
cea tech

Markets to be addressed: Automotive, 5G/6G, Health, Industry...



RTO's Topics of joint action (under discussion)

- Devices models
- Design enablement
- Low energy computing
- ...

Expected benefits of the Pilot line

- Enhanced support to industry ecosystems
- Easy and open design environment
- Mastering the N+2 and N+3 gen in EU, and the corresponding IPs
- Exploration of disruptive technologies (Neuromorphic, Quantum, ...)
- Prototyping & testing innovative design and system integration

→ Next step in RTO's cooperation supporting EU industry players

→ Acceleration of EU innovation via efficient Lab2Fab processes & tools